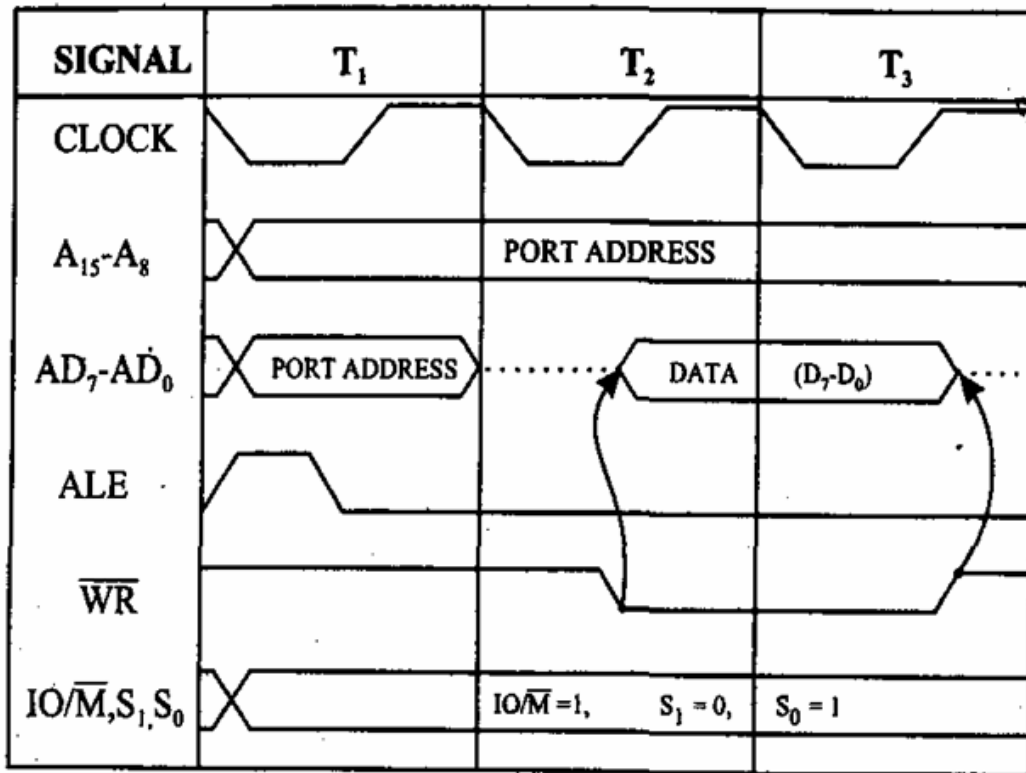


MEMORY READ CYCLE



MEMORY WRITE CYCLE

LDA 16bit Addr

