

Assignment - 3 (Digital Circuit)

ECE - 4th sem

- Que:-1. What do you mean by combinational and sequential circuit? Explain with Example.
- Que:-2. Explain and implement Half-subtractor and Full adder by logic gates.
- Que:-3. Define MUX and De-MUX. Discuss about its applications. Also, Design a 8-to-1 MUX with logic gates.
- Que:-4. Implement the Boolean function $F(A, B, C, D) = \sum_m(0, 1, 3, 4, 8, 9, 15)$ by using Multiplexer.
- Que:-5. What do you mean by Decoder? Explain and design — (i) 3-to-8 decoder
(ii) BCD-to seven segment decoder
- Que:-6. Implement 16-to-1 MUX by using two 8-to-1 Multiplexers (MUX).

Note:- (i) After completing assignment-3, send it at — Email: pceece2017@gmail.com

(ii) Go through books — (a) S. Salivahanan
(b) Floyd and Jain


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