

trans gate pulse
a diode...

14/6/2011

Inverters

→ It converts Fixed DC to variable AC.

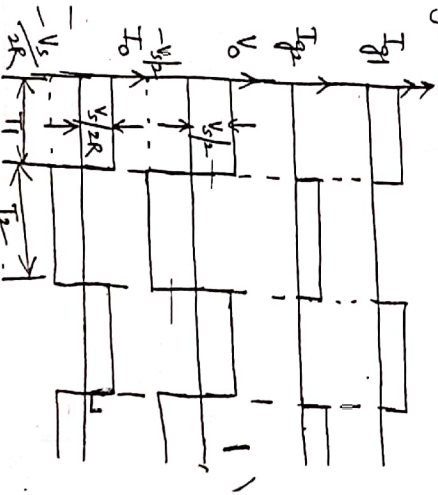
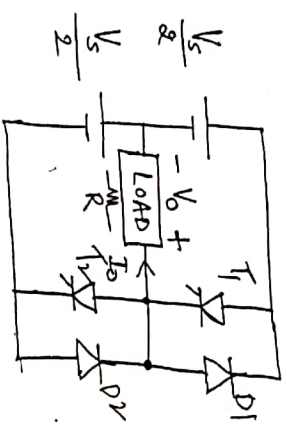
~~(VSI)~~

Classification of Inverters

- ① Voltage Source Inverters (VSI)
 - ① 1- ϕ Half bridge Inverter
 - ② 1- ϕ Full bridge Inverter
 - ③ 3- ϕ VSI
- ② Current Source Inverter (CSI)
 - ③ Series Inverter
 - ④ Parallel Inverter

Voltage Source Inverters

1. 1- ϕ Half bridge Inverter



(2)

$$1. \quad T_1 \rightarrow \text{ON}$$

$$V_o = \frac{V_s}{2}$$

$$I_o = \frac{V_s}{2R}$$

$$2. \quad T_2 \rightarrow \text{ON}$$

$$\therefore V_o = -\frac{V_s}{2}$$

$$I_o = -\frac{V_s}{2R}$$

$$\rightarrow V_{or} = \frac{V_s}{2}$$

$\rightarrow$ Forced commutation is required for

Resistive load.

$\rightarrow$ Feed back diodes won't conduct for resistive load.

Harmonic Analysis for o/p v_g waveform

(2)

Finding the Fourier series for the o/p waveform, we get,

$$V_o = \sum_{n=1,3,5,\dots}^{\infty} \frac{4 \left(\frac{V_s}{2} \right)}{n\pi} \sin n\omega t$$

$$\Rightarrow V_o = \sum_{n=1,3,5}^{\infty} \frac{2V_s}{n\pi} \sin n\omega t$$

$$= \frac{2V_s}{\pi} \left[\sin \omega t + \frac{\sin 3\omega t}{3} + \frac{\sin 5\omega t}{5} + \dots \right]$$

$$V_{on} = \frac{2V_s}{n\pi} \sin n\omega t$$

$$(V_{on})_{rms} = \frac{2V_s}{n\pi} \cdot \frac{1}{\sqrt{2}} = \frac{\sqrt{2}V_s}{n\pi}$$

$$(V_{o1})_{rms} = \frac{\sqrt{2}V_s}{\pi}$$

$$\text{Distortion Factor} = \frac{(V_{o1})_{rms}}{V_{or}} = \frac{\frac{\sqrt{2}V_s}{\pi}}{\frac{2V_s}{2}} = \frac{2\sqrt{2}}{\pi}$$

→ g value depends only on the shape of the waveform.

(4)

$$\text{THD} = \left(\frac{1}{g^2} - 1 \right)^{1/2}$$

$$= \sqrt{\frac{V_r^2}{8} - 1}$$

$$= 0.4834$$

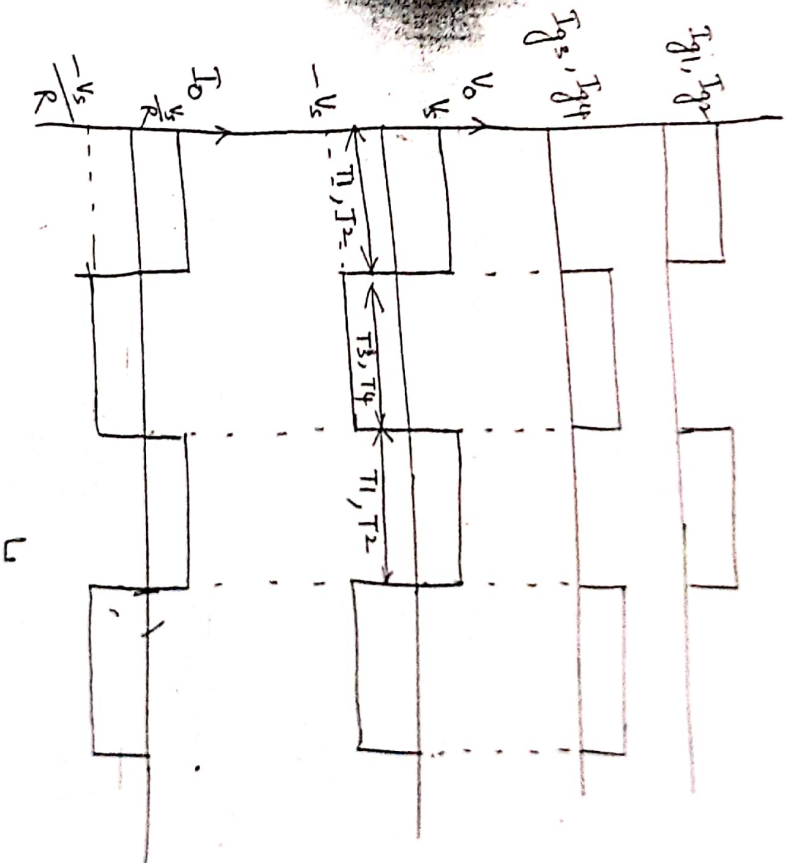
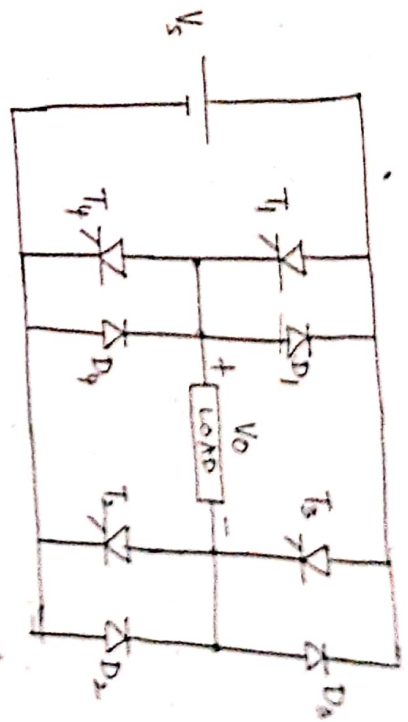
$$\boxed{\text{T.H.D.} = 48.34\%}$$

→ T.H.D. is a measure of harmonics in AC waveform.

* → In a VSI, of V_g waveform remains same irrespective of the load. The load nature of current magnitude & load waveform changes as the load nature of the load changes.

1- ϕ Full bridge Inverter

(4)



1) $T_1, T_2 \rightarrow ON$

$$V_o = V_s$$

$$I_o = \frac{V_s}{R}$$

2) $T_3, T_4 \rightarrow ON$

$$V_o = -V_s$$

$$I_o = -\frac{V_s}{R}$$

$$V_{or} = \frac{V_s}{2}$$

$$V_o = \sum_{n=1,3,5}^{\infty} \frac{4V_s}{n\pi} \sin n\omega t$$

$$= \frac{4V_s}{\pi} \left[\sin \omega t + \frac{\sin 3\omega t}{3} + \dots \right]$$

$$V_{on} = \frac{4V_s}{n\pi} \sin n\omega t$$

$$(V_{on})_{rms} = \frac{2\sqrt{2}V_s}{n\pi}$$

$$(V_{o1})_{rms} = \frac{2\sqrt{2}V_s}{\pi}$$

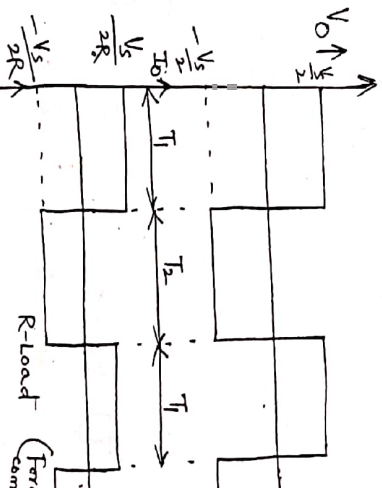
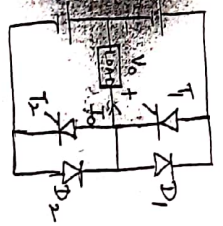
6

$$g = \frac{(V_{o1})_{rms}}{V_{or}}$$

$$= \left[\frac{2\sqrt{2}V_s}{\pi} \right] \frac{1}{\frac{V_s}{2}} = \frac{2\sqrt{2}}{\pi}$$

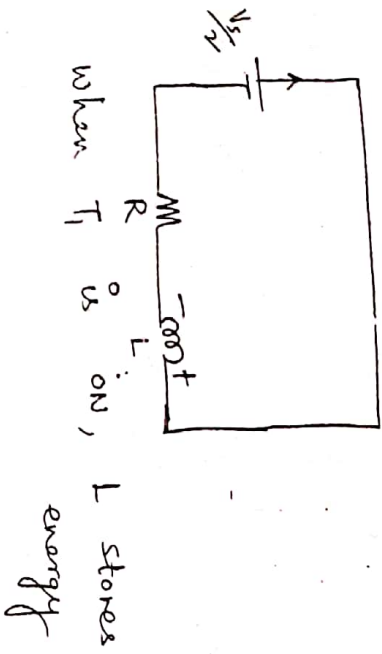
$$T.H.D = \sqrt{\frac{1}{g^2} - 1} = 48.34\%$$

1a) 1- ϕ half bridge Inverter



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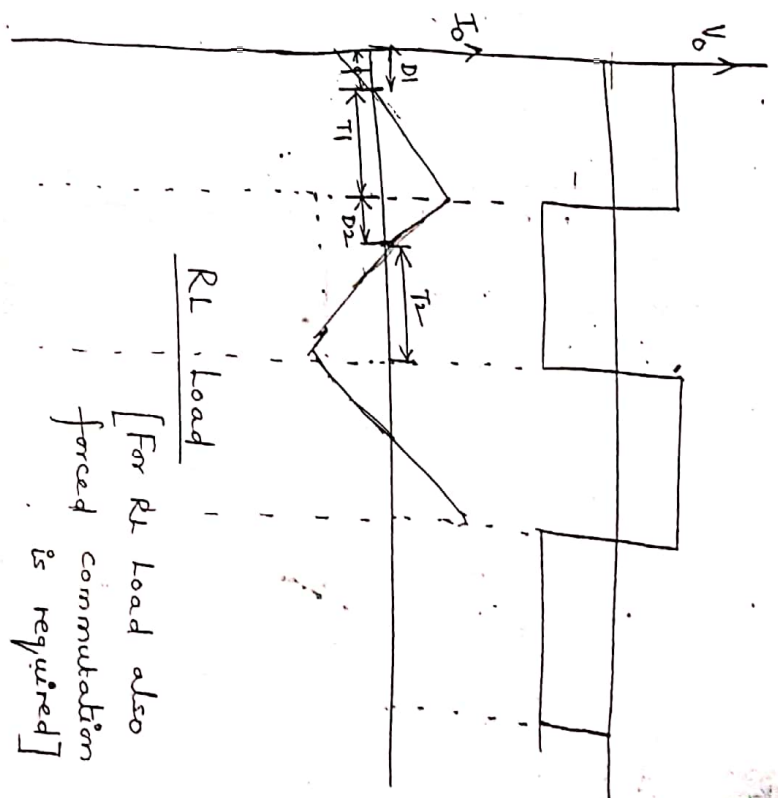
→ For RL Load I_o lags V_o by $\phi = \tan^{-1}\left(\frac{\omega L}{R}\right)$



⇒ Power flows from source to load
 $\left(\frac{1}{2} Li^2 + i^2 R\right)$

(i) When thyristor is ON, Power is stored in $\therefore L$ stores energy

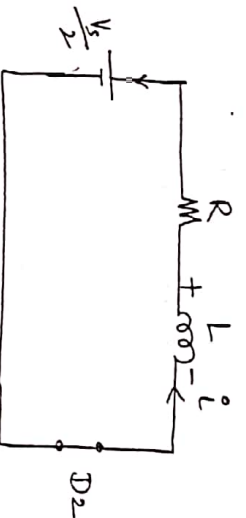
(ii) The moment thyristor is forced commutated, inductor starts releasing its energy to source through Diode.



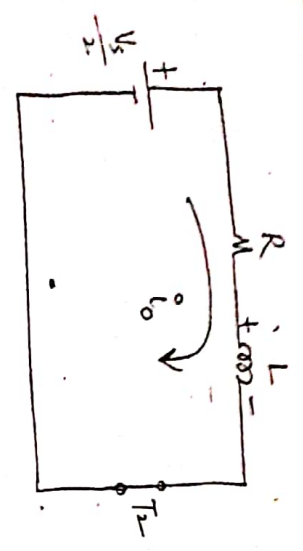
[For RL load also forced commutation is required]

Mode 2 D_2 is ON

$$\frac{1}{2} Li^2 \rightarrow P_{source} + i^2 R$$

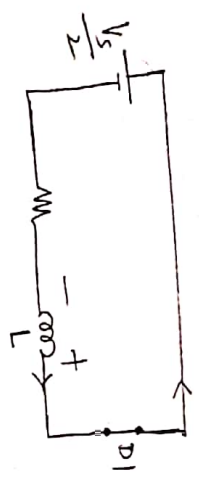


Mode 3: T_2 is ON



$P_{source} \rightarrow \frac{1}{2} Li_o^2 + i_o^2 R$

Mode 4: D_1 is ON



$\frac{1}{2} Li_o^2 \rightarrow P_{source} + i_o^2 R$

(19)

$T_1 \rightarrow ON$
or $D_1 \rightarrow ON$

$V_o = \frac{V_s}{2}$

i.e., V_o is +ve

$T_2 \rightarrow ON$
or $D_2 \rightarrow ON$

$V_o = -\frac{V_s}{2}$

i.e., V_o is -ve

(20)

Logic Table

Series Conv
 $\frac{1}{2}$ Bridge full bridge

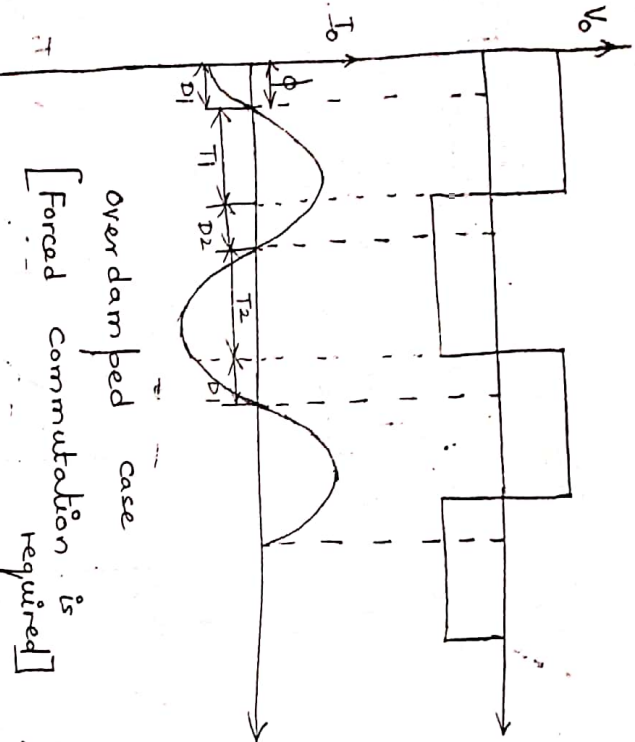
V_o	I_o	P	$\frac{1}{2}$ Bridge	Full bridge
+	+	+	T_1	T_1, T_2
$(\underline{T}_1, \underline{D}_1)$	-	-	D_1	D_1, D_2
$(T_1, \underline{D}_1)$	-	+	T_2	T_3, T_4
-	+	-	D_2	D_3, D_4
$(\underline{T}_2, \underline{D}_2)$				
$(T_2, \underline{D}_2)$				

(12)

(iii) RLC (overdamped case) -

i.e., ($X_L > X_C$)

I_o lags V_o by $\phi = \tan^{-1} \left(\frac{X_L - X_C}{R} \right)$



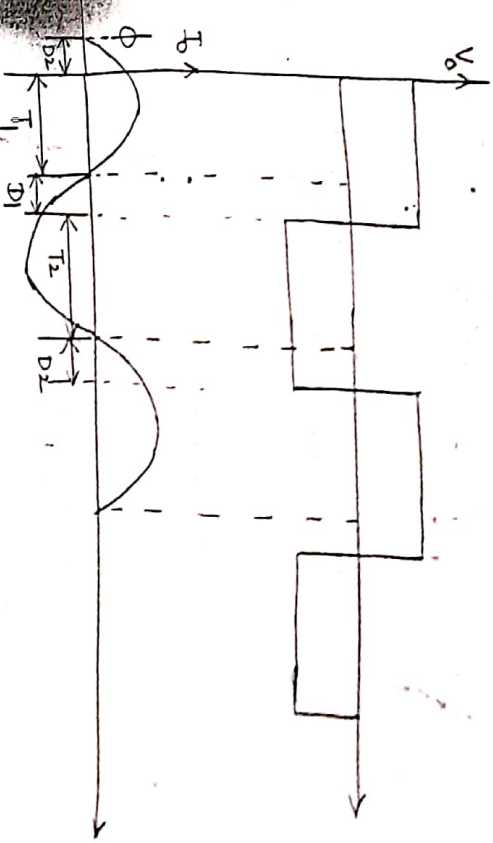
(13)

RLC (Underdamped case)

i.e., $X_C > X_L$ (or $R^2 < \frac{4L}{C}$)

I_o leads V_o by $\tan^{-1} \left(\frac{X_C - X_L}{R} \right)$

(or I_o lags V_o by $\tan^{-1} \left(\frac{X_L - X_C}{R} \right)$)



→ In this case t_c of π is same as
Conduction time of $D1$ i.e., $\frac{\phi}{\omega}$

$$\text{i.e., } t_c = \frac{\tan^{-1} \left(\frac{X_C - X_L}{R} \right)}{\omega}$$

(14)

→ If $t_c > t_q$, - forced commutation is not required because load commutation takes place.

→ If $t_c < t_q$, forced commutation is required.

Harmonic analysis for op current for a generalised (half-bridge) load:

→ Let RIC be the generalized load.

$$V_{on} = \frac{2V_s}{n\pi} \sin n\omega t$$

$$Z_n = R + j(X_{Ln} - X_{Cn})$$

$$X_{Ln} = n\omega L$$

$$X_{Cn} = \frac{1}{n\omega C}$$

$$|Z_n| = \sqrt{R^2 + (X_{Ln} - X_{Cn})^2}$$

(15)

$$I_{on} = \frac{V_{on}}{|Z_n|} \angle -\phi_n$$

$$\phi_n = \tan^{-1} \left(\frac{X_{Ln} - X_{Cn}}{R} \right)$$

Here ϕ_n is n^{th} harmonic displacement angle.

$$I_{on} = \frac{2V_s}{n\pi |Z_n|} \sin(n\omega t - \phi_n)$$

⇒ For pure inductive load,

$$|Z_n| = n\omega L$$

$$\phi_n = 90^\circ$$

$$I_{on} = \frac{V_{on}}{n\omega L} \angle -90^\circ$$

$$= \frac{2V_s}{n\pi(n\omega L)} \sin(n\omega t - 90^\circ)$$

$$= \frac{2V_s}{n^2(\pi\omega L)} \sin(n\omega t - 90^\circ)$$

[For square waveform, n^{th} harmonic $\propto \frac{1}{n}$
Triangular " , n^{th} harmonic $\propto \frac{1}{n^2}$]

(16)

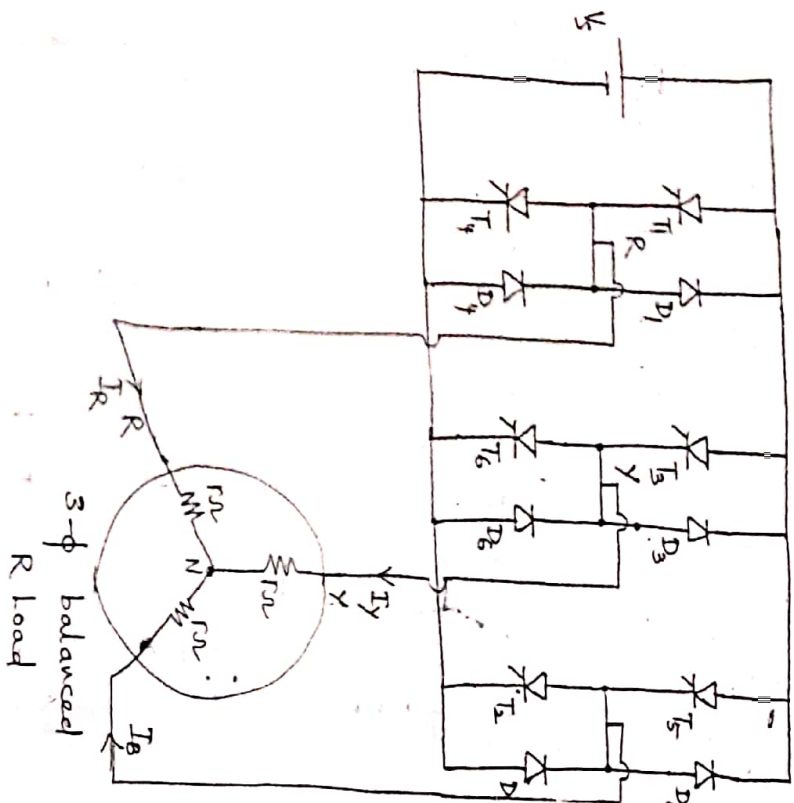
→ In case of Full bridge inverter

$$V_{on} = \frac{4V_s}{\pi}$$

Rest of the expressions remain same.

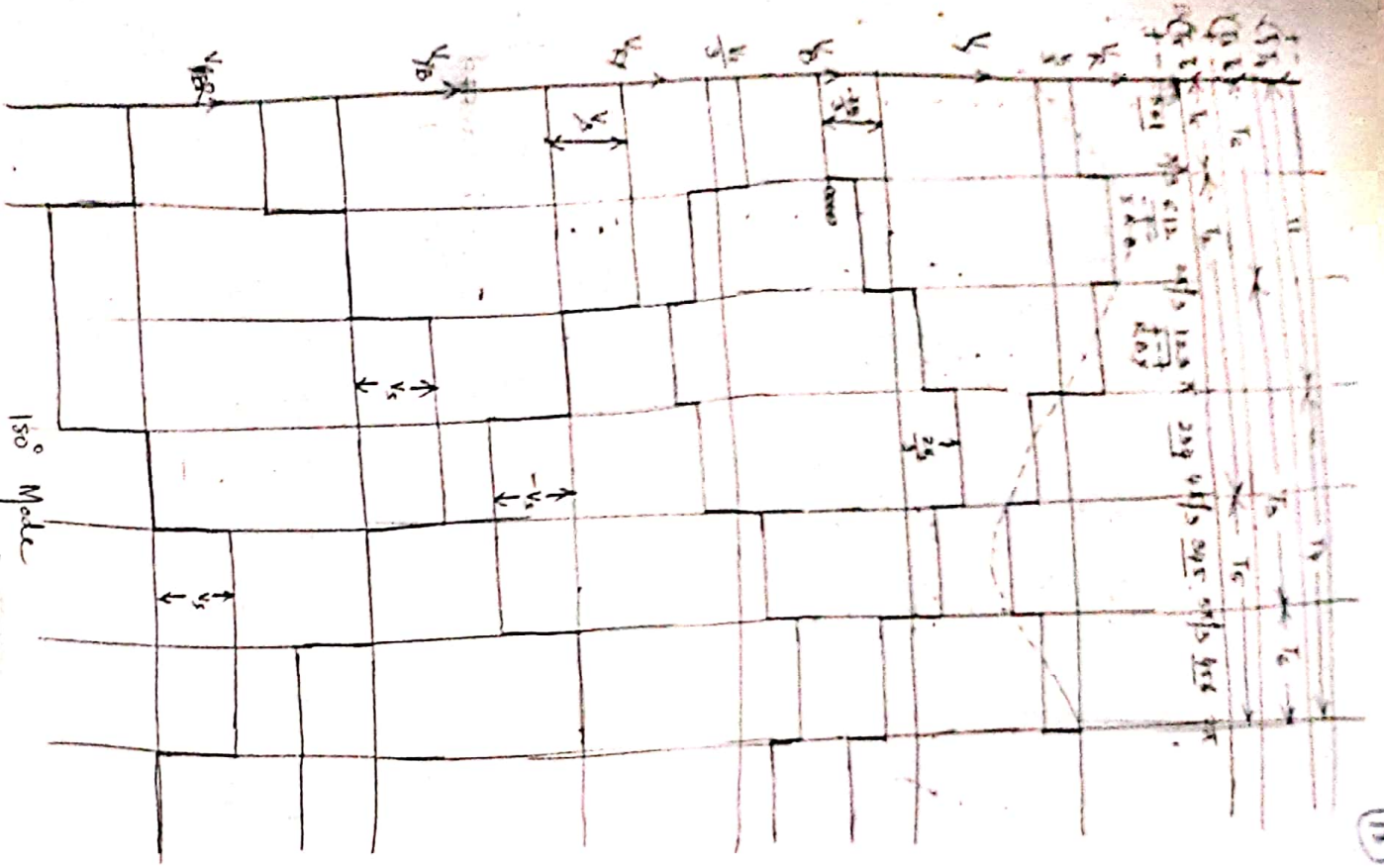
3- ϕ Voltage Source Inverter

(17)



$T_1, T_3, T_5 \Rightarrow$ +ve group SCRs
 $T_4, T_6, T_2 \Rightarrow$ -ve group SCRs

(15)



In 150° mode, each SCR conducts for 150°

(i) 150° Mode (R Y B)



→ In 150° mode, at every instant 2 SCRs are conducting together

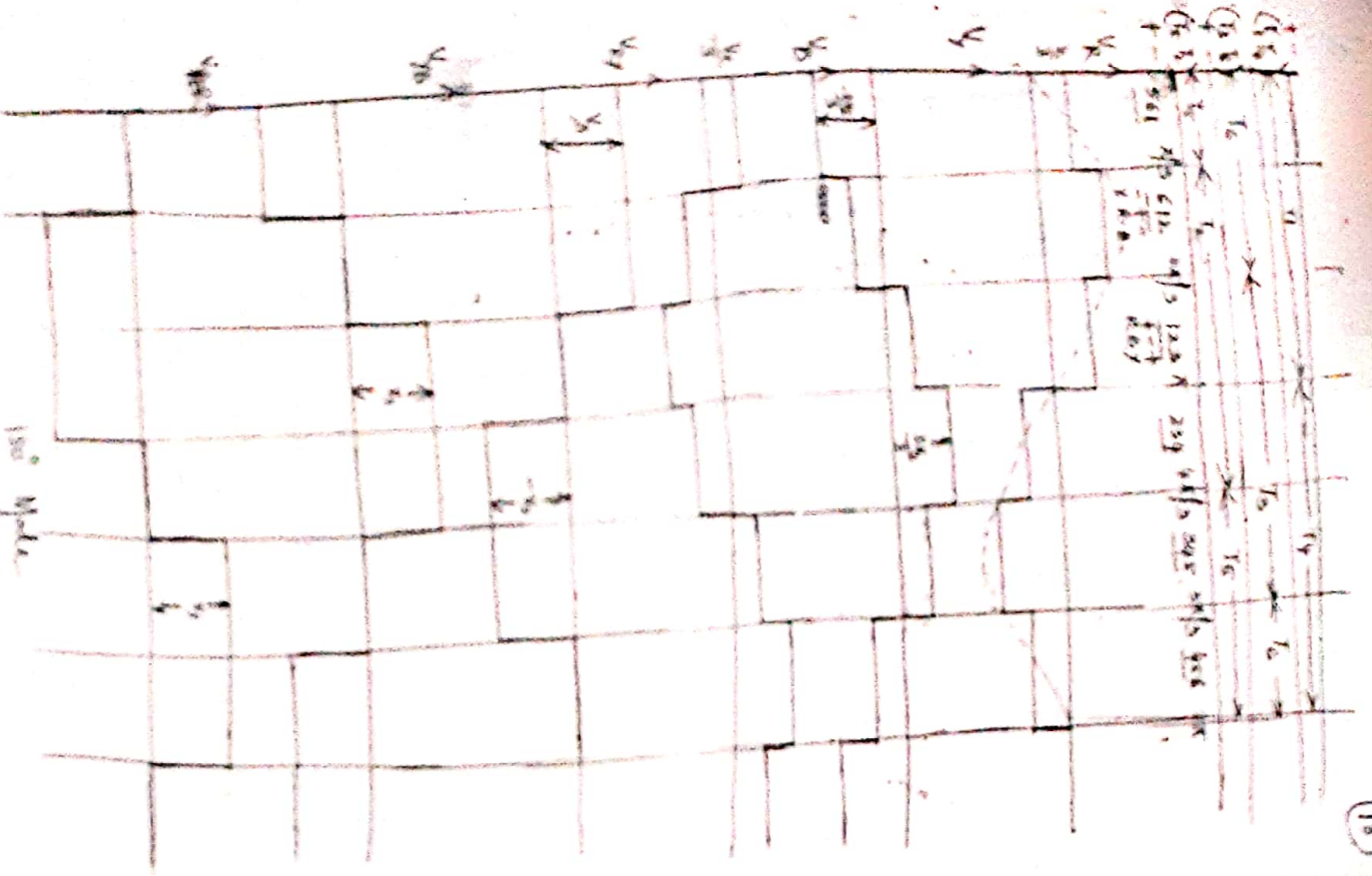
Mode 1 : 0 to $\pi/3$

T_r, T_c, T_1 are conducting



i.e., one -ve group SCR of Y-phase is short-circuited at this time

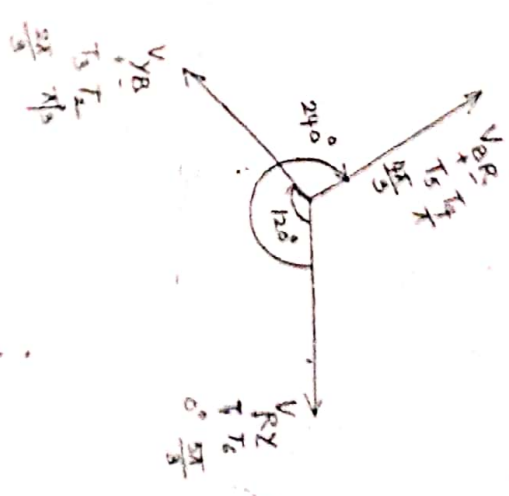
(18)



(19)

In 180° mode, each SCR conducts for 180°

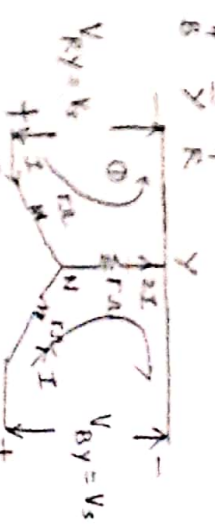
(i) 180° Mode (R/Y/B)



→ In 180° mode, at every instant 3 SCRs are conducting together.

Mode 1 : 0 to $\pi/3$

T_1, T_2, T_3 are conducting.



i.e., one we graft starting of the two thyristors

(20)
i.e., current passing thro' γ -phase is double the current thro' other phases.

$$KVL \Rightarrow (IR) + (2IR) = V_s$$

$$\Rightarrow IR = \frac{V_s}{3}$$

$$\Rightarrow V_R = R \cdot I = \frac{V_s}{3}$$

$$\text{Hby } V_Y = -\cancel{2I} \cdot R \quad 2I \cdot R \\ = -\frac{2V_s}{3}$$

$$V_B = R \cdot I = \frac{V_s}{3}$$

$$(V_R)_{rms} =$$

$$= \sqrt{\frac{1}{\pi} \left\{ \left(\frac{V_s^2}{3} \right) \cdot \frac{\pi}{3} + \left(\frac{2V_s^2}{3} \right) \cdot \frac{\pi}{3} + \left(\frac{V_s^2}{3} \right) \cdot \frac{\pi}{3} \right\}}$$

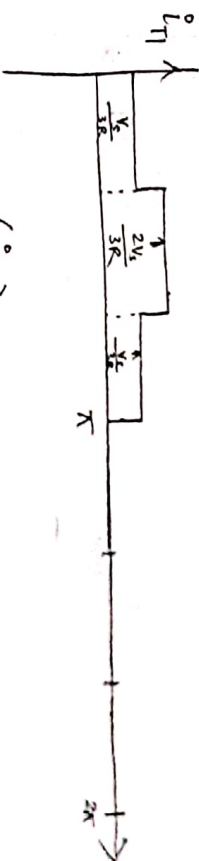
$$= \sqrt{\frac{1}{3} \left\{ V_s^2 \left(\frac{1}{9} + \frac{4}{9} + \frac{1}{9} \right) \right\}}$$

$$= \sqrt{\frac{V_s^2}{3} \cdot \frac{6}{9}}$$

$$= \frac{\sqrt{2} V_s}{3} = (V_Y)_{rms} = (V_B)_{rms}$$

$$(I_R = I_Y = I_B = I_{PR} = I_L)_{rms} = \frac{\sqrt{2} V_s}{3R} \text{ Amp}$$

$$(I_{\pi})_{rms} =$$



$$(i_{\pi})_{rms} =$$

(22)

$$(i_T)_{rms}$$

$$= \left[\frac{1}{2\pi} \left[\left(\frac{V_s}{3R} \right)^2 \cdot \frac{\pi}{3} + \left(\frac{2V_s}{3R} \right)^2 \cdot \frac{\pi}{3} + \left(\frac{V_s}{3R} \right)^2 \cdot \frac{\pi}{3} \right] \right]^{1/2}$$

$$= \frac{V_s}{3R} \text{ Amp.}$$

$$(I_T)_{rms} = \frac{V_s}{3R} \text{ Amp.}$$

$$(V_R)_{rms} = V_s \sqrt{\frac{2}{3}}$$

$$\boxed{\begin{aligned} (V_{ph})_{rms} &= \frac{\sqrt{2} V_s}{3} \\ (V_L)_{rms} &= V_s \sqrt{\frac{2}{3}} = \sqrt{3} (V_{ph})_{rms} \end{aligned}}$$

(23)

Harmonic Analysis for phase lag:

Finding the Fourier series expansion for R-phase, we get,

$$V_R = \sum_{n=6K \pm 1}^{\infty} \frac{2V_s}{n\pi} \sin n\omega t$$

where $K = 0, 1, 2, 3, \dots$

i.e., $n = 1, (5, 7), (11, 13), \dots$

NOTE: Here even harmonics & triplen harmonics are absent.

$$(V_{Rn}) = \frac{2V_s}{n\pi} \sin n\omega t$$

$$(V_{Rn})_{rms} = \frac{\sqrt{2} V_s}{n\pi}$$

$$(V_{R1})_{rms} = \frac{\sqrt{2} V_s}{\pi}$$

$$g = \frac{(V_{R1})_{rms}}{(V_R)_{rms}} = \frac{\frac{\sqrt{2} V_s}{\pi}}{\frac{\sqrt{2} V_s}{3}} = \frac{3}{\pi}$$

(24)

$$T.H.D = \sqrt{\frac{1}{g} - 1}$$

$$= \sqrt{\frac{\pi^2}{9} - 1}$$

$$= \frac{31.08}{100}$$

→ For a generalised load

$$I_{Rn} = \frac{V_{Rn}}{Z_n} \angle -\phi_n$$

$$V_{Rn} = \frac{2V_s}{n\pi} \sin n\omega t$$

$$\rightarrow I_{Rn} = \frac{2V_s}{n\pi |Z_n|} \sin(n\omega t - \phi_n)$$

$$Z_n = \sqrt{R^2 + (X_{Ln} - X_{Cn})^2}$$

$$\phi = \tan^{-1} \left(\frac{X_{Ln} - X_{Cn}}{R} \right)$$

L

(25)

Harmonic analysis for line vg waveform:

Applying Fourier series for line vg V_{RY} , we get,

$$V_{RY} = \sum_{n=1,3,\dots}^{\infty} \frac{4V_s}{n\pi} \cos \frac{n\pi}{6} \sin(n\omega t + \frac{n\pi}{6})$$

Note: Here also even harmonics & triple harmonics are absent.

$$(V_{RY})_{rms} = \frac{2\sqrt{2}V_s}{n\pi} \cos \frac{n\pi}{6}$$

$$(V_{RY})_{rms} = \frac{2\sqrt{2}V_s}{\pi} \cos \frac{\pi}{6}$$

$$= \frac{\sqrt{6}}{\pi} \cdot V_s$$

$$g = \frac{(V_{RY})_{rms}}{(V_{RY})_{rms}} = \frac{\frac{\sqrt{6}}{\pi} \cdot V_s}{V_s \cdot \frac{\sqrt{3}}{\pi}}$$

$$= \frac{3}{4}$$

(26)

$$\therefore T.H.D. = \sqrt{\frac{1}{g^2} - 1}$$

$$= \sqrt{\frac{\pi^2}{9} - 1}$$

$$\approx 31.08\%$$

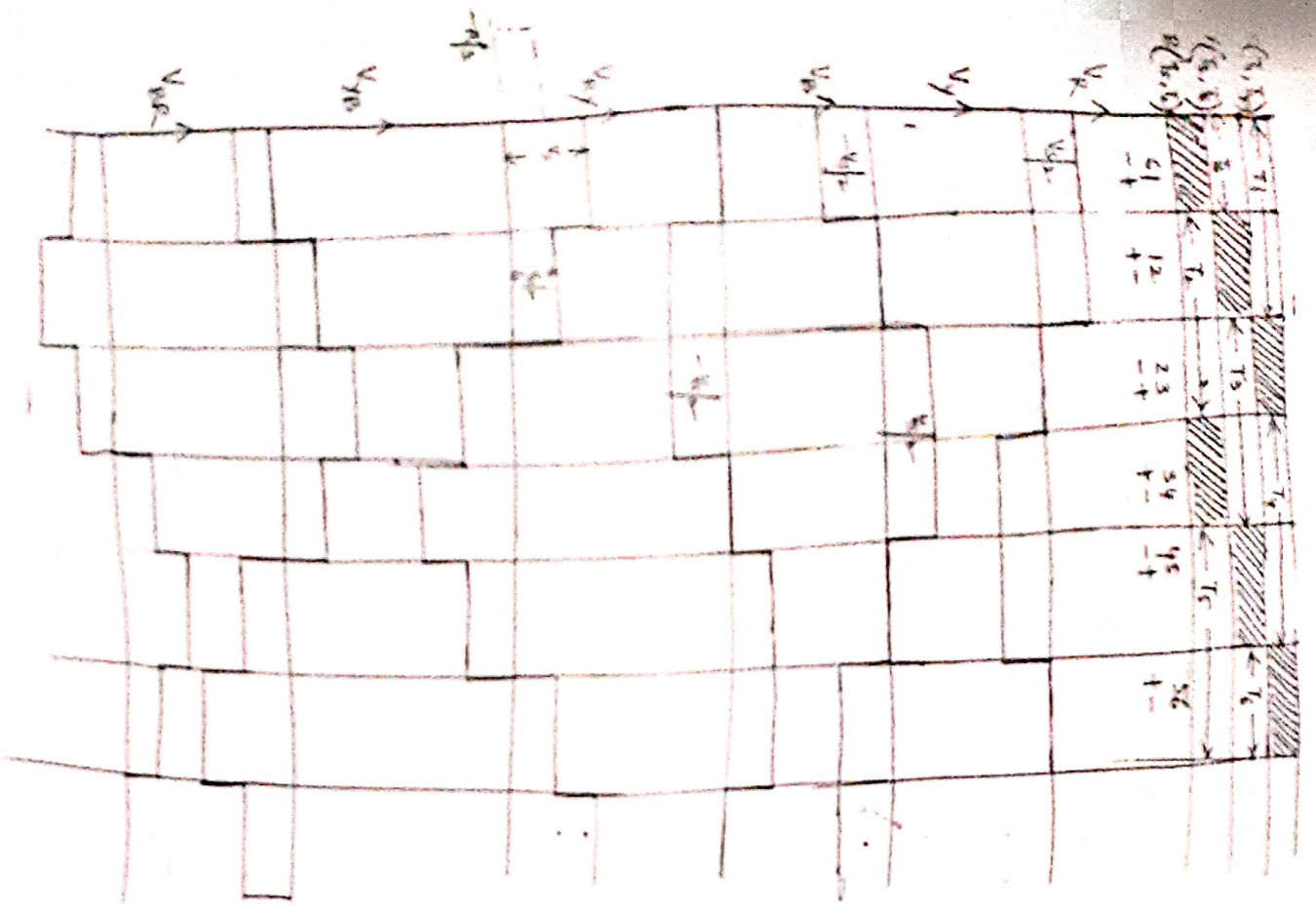
Drawback of 180° mode:

There is a possibility of dead short circuit when the SCR starts conducting outgoing SCR belonging to the same phase stops conducting before the incoming SCR starts conducting.

120° Mode

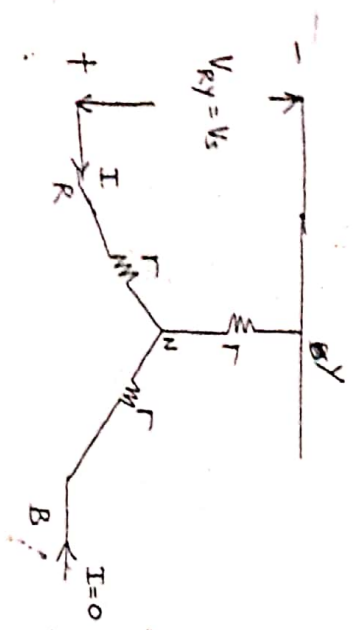
In 120° mode, each SCR is allotted conduction for 120° & the last 60° is allotted for commutation period.

(27)



In Mode 1: 0 to 60°

T_1, T_6 are conducting.



$$I(r+r) = V_s$$

$$\Rightarrow I_r = \frac{V_s}{2}$$

$$\therefore V_R = \frac{V_s}{2} ; V_Y = -\frac{V_s}{2}$$

$$V_B = 0$$

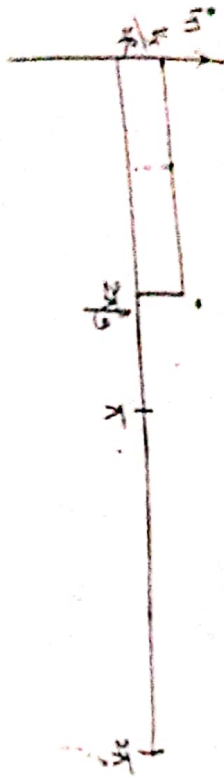
$$(V_R)_{rms} = \frac{V_s}{2} \cdot \frac{\sqrt{2}}{\sqrt{3}} = \frac{V_s}{\sqrt{6}} = (V_Y)_{rms}$$

$$= (V_B)_{rms}$$

$$= (V_{ph})_{rms}$$

$$I_R = I_Y = I_D = I_{ph} = I_L \text{ rms}$$

$$= \frac{V_s}{\sqrt{3} R} \text{ Amps}$$



$$(I_n)_{rms} = \frac{V_s}{2\sqrt{3} R} \cdot \sqrt{\frac{1}{3}}$$

$$= \frac{V_s}{2\sqrt{3} R} \text{ Amps}$$

$$\rightarrow (V_L)_{rms} = \sqrt{3} \cdot (V_{ph})_{rms}$$

$$= \frac{V_s}{\sqrt{2}}$$

Harmonic Analysis of phase v_g :

$$= \sum_{n=1,3,5}^{\infty} \frac{2V_s}{n\pi} \cos \frac{n\pi}{6} \sin(n\omega t + n\frac{\pi}{6})$$

i.e., Even & Triplen harmonics are absent

$$\theta = \frac{\pi}{3} \text{ \& T.U.D.} \approx 31.4^\circ$$

Harmonic Analysis of line v_g :

$$= \sum_{n=1,3,5}^{\infty} \frac{2V_s}{n\pi} \sin(n\omega t + n\frac{\pi}{6})$$

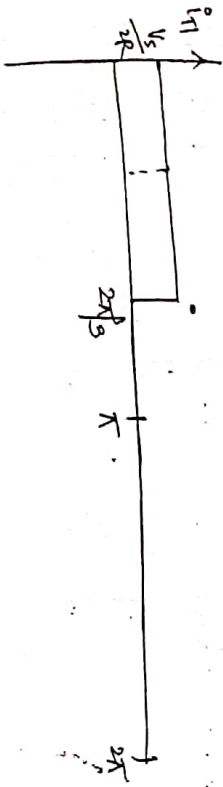
i.e., Even & Triplen harmonics are absent

$$\theta = \frac{\pi}{3} \text{ \& T.U.D.} \approx 31.4^\circ$$

(30)

$$I_R = I_Y = I_B = I_{ph} = I_L)_{rms}$$

$$= \frac{V_s}{\sqrt{6} \cdot r} \text{ Amps.}$$



$$(I_{II})_{rms} = \frac{V_s}{2\sqrt{3}r} \cdot \sqrt{\frac{1}{3}}$$

$$= \frac{V_s}{2\sqrt{3}r} \text{ Amps.}$$

$$\rightarrow (V_L)_{rms} = \sqrt{3} \cdot (V_{ph})_{rms} = \frac{V_s}{\sqrt{2}}$$

(31)

Harmonic Analysis of phase v_{g0}

$$= \sum_{n=1,3,5}^{\infty} \frac{2V_s}{n\pi} \cos \frac{n\pi}{6} \sin(n\omega t + \pi/6)$$

ie, Even & Triplen harmonics are absent

$$g = \frac{3}{\pi} \quad \& \quad T.H.D. \approx 31\%$$

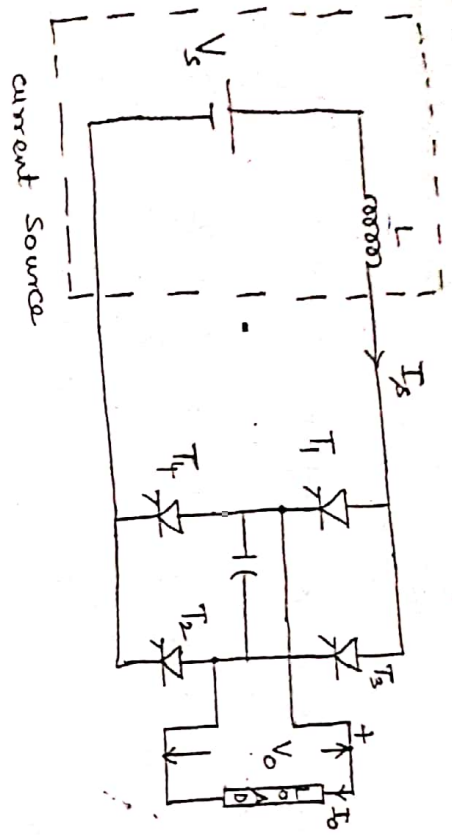
Harmonic Analysis of line v_{g0}

$$= \sum_{n=6k \pm 1}^{\infty} \frac{3V_s}{n\pi} \sin(n\omega t + \pi/3)$$

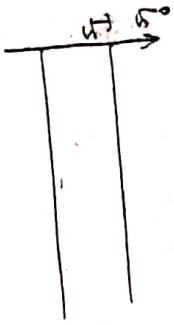
ie, Even & Triplen harmonics are absent

$$g = \frac{3}{\pi} \quad \& \quad T.H.D. \approx 31\%$$

3. Current Source Inverter



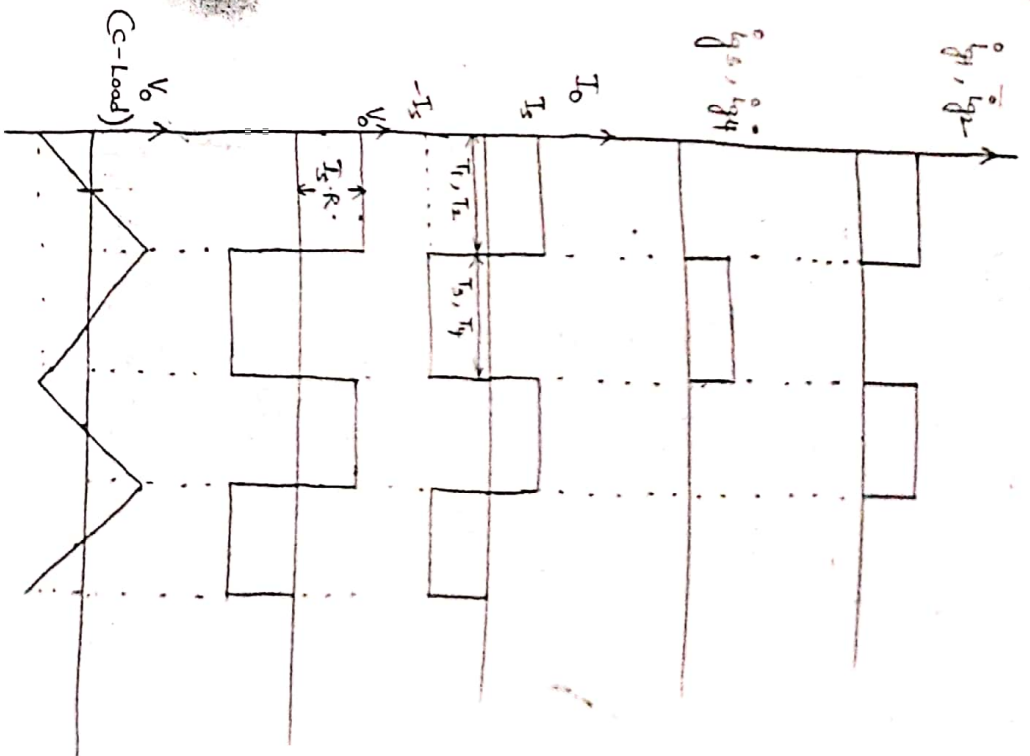
Capacitor is provided for the purpose of commutation.



→ In CSI, we consider I_0 as reference where as in VSI, V_0 is the reference

L

(32)



$$I_{or} = I_s$$

For C-load
 $V_0 \propto I_0$
 $I_0 \propto I_s$
 $V_0 \propto I_s$

(33)

Harmonic analysis for generalized load: (24)

$$I_o = \sum_{n=1,3,5}^{\infty} \frac{4I_s}{n\pi} \sin n\omega t$$

$$I_{on} = \frac{4I_s}{n\pi} \sin n\omega t$$

$$\Rightarrow \rho = \frac{2\sqrt{2}}{\pi}$$

$$\therefore \text{T.H.D} = 48.34\%$$

For generalised load,

$$V_{on} = I_{on} \cdot Z_n$$

$$= I_{on} \angle \phi_n |Z_n|$$

$$= \frac{4I_s}{n\pi} |Z_n| \sin(n\omega t + \phi_n)$$

$$\phi_n = \tan^{-1} \left(\frac{X_{Ln} - X_{Cn}}{R} \right)$$

$$|Z_n| = \sqrt{R^2 + (X_{Ln} - X_{Cn})^2}$$

→ For pure capacitive load,

$$Z_n = \frac{1}{n\omega C}$$

$$\phi_n = -90^\circ$$

$$\therefore V_{on} = \frac{4I_s}{n\pi} \cdot \frac{1}{n\omega C} \sin(n\omega t - 90^\circ)$$

$$V_{on} \propto \frac{1}{n^2} \quad (\text{Hence, Triangular wave})$$

→ The technique employed in CSI is complementary commutation technique.

→ If the load is purely capacitive then there is no need of commutating capacitor.

Advantages of CSI

- 1. Feedback diodes are not required in CSI.

② Commutation is simple for CSI -
 * 3. There is a possibility of load is capacitive. Commutation if the load is

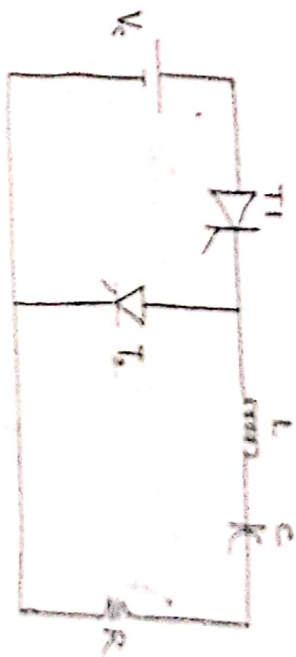
Disadvantage of CSI:

The commutating capacitor applies a very high reverse V_r across the power semiconductor device used in CSI. Therefore, the devices having low reverse blocking capability such as BJT, MOSFET, IGBT, GTO are not generally preferred in CSI. Therefore, we generally prefer SCR for CSI because of high blocking capability.

→ CSI is preferable in case of capacitive loads ∵ commutation becomes simple in that case.

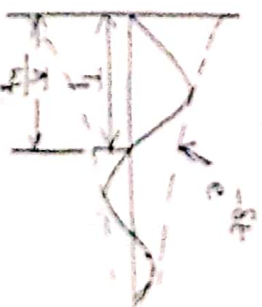
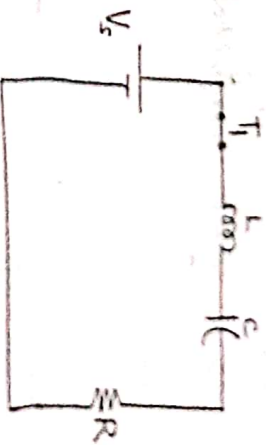
→ Here RLC should satisfy underdamped condition to make the commutation possible.

Series Inverter



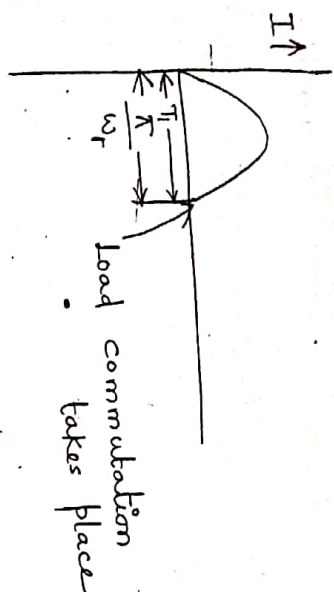
Note: T_1 is ON

L/C : Commutating element



$$I = \frac{V_s}{\omega_p L} e^{-\delta t} \sin \omega_p t$$

$$\omega_r = \text{Ringing frequency} = \sqrt{\frac{1}{LC} - \frac{R^2}{4L^2}}$$

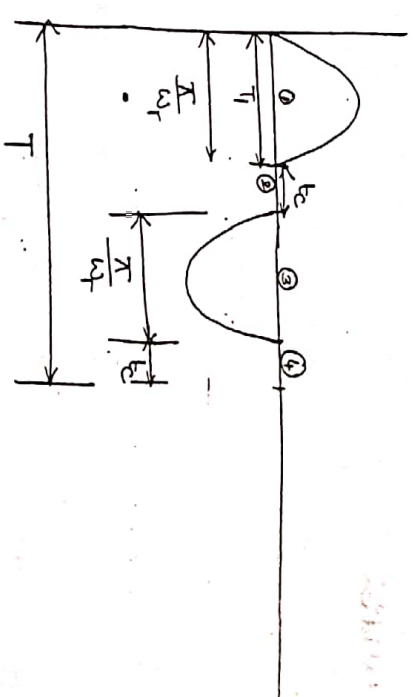
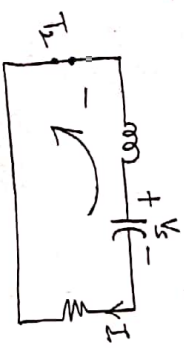


Mode 1: T_2 is switched ON
ALLOTMENT OF COMMUTATION TIME
 FOR T_1

~~For T_2~~

If we switch on T_2 immediately, i.e., before T_1 stops conducting, it will short circuit the supply. $\therefore$ we need to provide some commutation time for T_1 in Mode 2

Mode 3: T_2 is switched ON



Mode 4: Allotment of commutation time for T_2

$$\frac{T}{2} = \frac{\pi}{\omega_r} + t_c$$

(Half Time period of the inverter)

$$T = 2 \left(\frac{\pi}{\omega_r} + t_c \right)$$

$$f = \left\{ \frac{1}{2 \left(\frac{\pi}{\omega_r} + t_c \right)} \right\} \text{ Hz.}$$

(40)

→ By reducing t_c , switching frequency f is increased & simultaneously harmonics are reduced.

→ For low frequencies the current waveform is distorted with severe harmonics. [∵ of high value of t_c]. But at high frequencies, with reduced t_c , the waveform distortion is less. Hence, series inverter is preferred generally for high freq. applications i.e., near 100 KHz.

→ Series inverters are generally preferred for fixed loads at high frequencies.

(41)

$$f_{\max} = \frac{1}{2\left(\frac{\pi}{\omega_r} + t_q\right)}$$

(∵ t_c can't be reduced beyond t_q)

→ Series inverters can't be used for wide range of applications. It is only restricted to use for fixed load at high switching frequency.

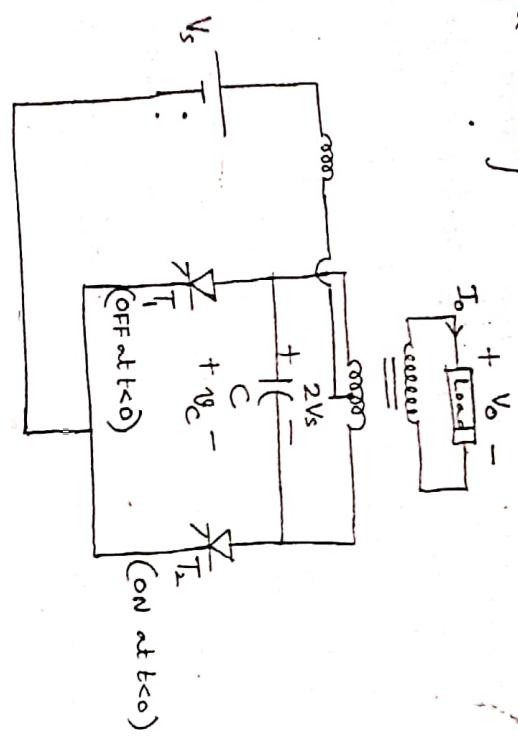
→ Here, commutating elements L & C are connected in series with the resistive load. Hence, it is called series inverter.

(42)

Parallel Inverter

In parallel inverter complementary commutation is employed.

Commutating capacitor is connected in parallel with the load through a transformer.



Assumption:

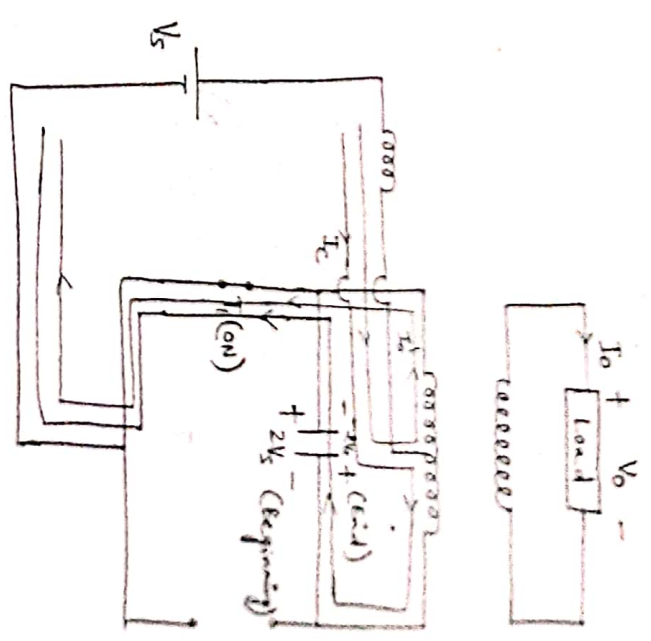
- 1. capacitor is initially charged with $2V_s$ volt with polarities shown in the fig. ($V_c(t=0) = 2V_s$)

(43)

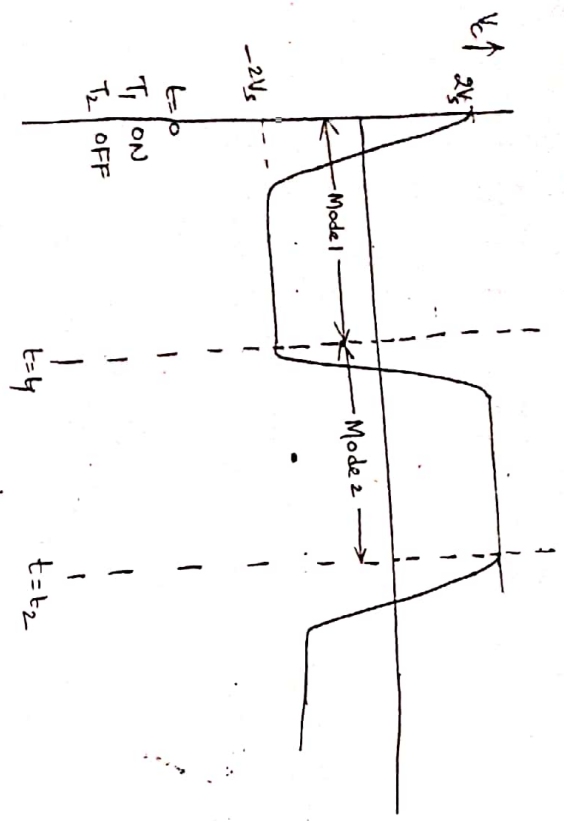
2. Before $t=0$, T_2 is in the ON state & T_1 is in the OFF state

Mode 1:

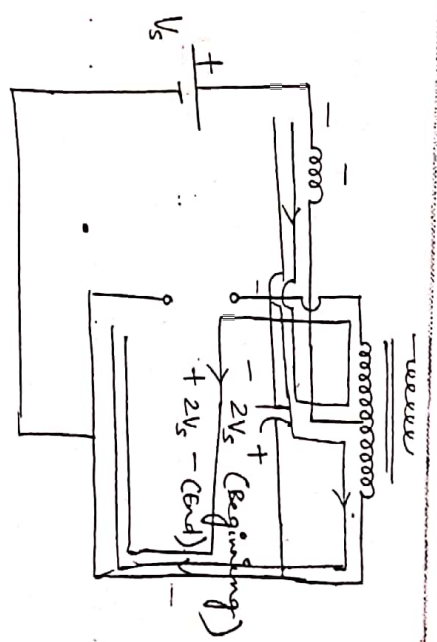
At $t=0$, T_1 is switched ON



$I_o = \text{load current}$
referred to primary side



Mode 2: T_2 is switched ON at $t = t_1$
 $v_o(t = t_1) = -2V_s$ (Beginning)



$$V_o = \frac{N_2}{N_1} \cdot 2V_s$$

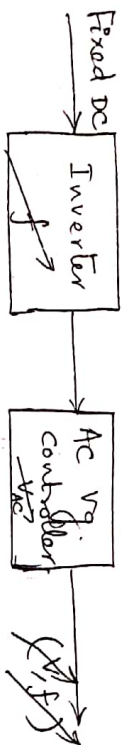
Shape of capacitor v_g & that of load v_g remains same.
 Parallel inverters are never used for low frequencies.

16/9/2011

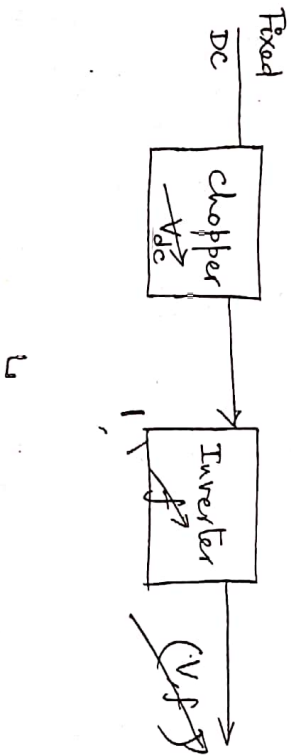
Voltage control of Inverters

→ In case of V_{SI} , V_g control is not possible only freq. control is possible.

External control:



AC V_g controller changes AC V_g level with out changing the frequency

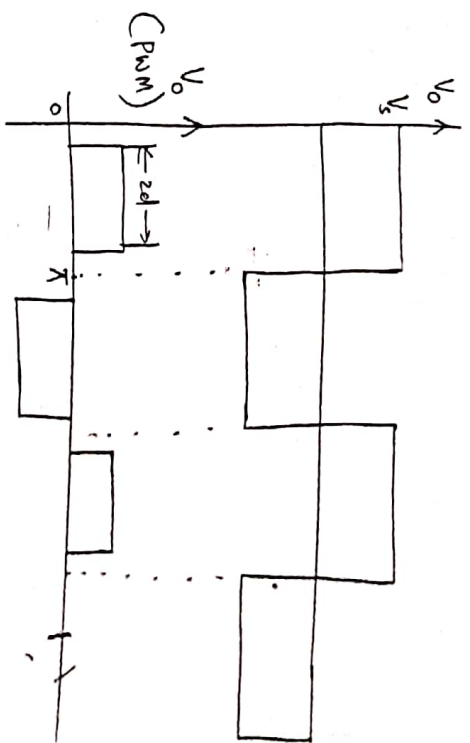


(46)

Drawback of External voltage control:

1. As the number of stages increase, there is an increase in additional cost & power loss along with the severe waveform distortion.
2. The overall setup in external control is not compact.

Internal Voltage control using PWM Technique:



By changing the pulse width in each half cycle, we can get variable rms V_g .

(47)

'2d' is Total pulse width in each half cycle.

$$V_{or} = V_s \sqrt{\frac{2d}{\pi}}$$

Advantages of PWM inverter:

1. Variable o/p v_g is possible within the inverter itself.
2. we can also eliminate some of the lower order harmonics using PWM techniques.