

7/9/2011

①

Inverters

→ It converts Fixed DC to Variable AC.
(V, f)

Classification of Inverters

① Voltage Source Inverters (VSI)

- (a) 1- ϕ Half bridge Inverter
- (b) 1- ϕ Full bridge Inverter
- (c) 3- ϕ VSI

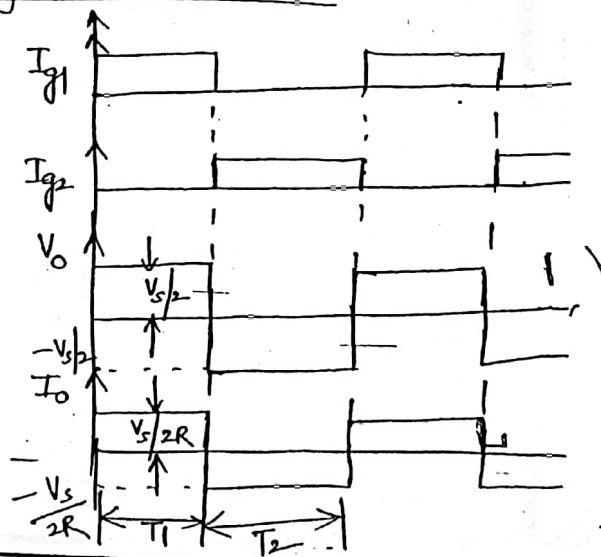
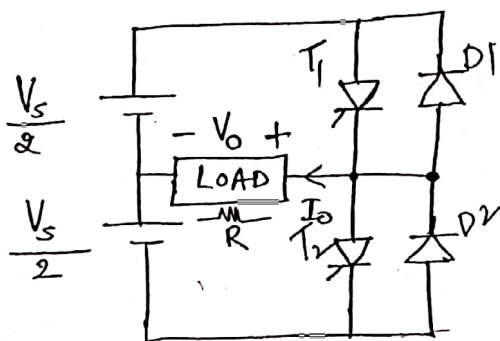
② Current Source Inverter (CSI)

③ Series Inverter

④ Parallel Inverter

Voltage Source Inverters

1. 1- ϕ Half bridge Inverter



(2)

$$1. \quad \underline{I_1 \rightarrow ON}$$

$$V_0 = \frac{V_s}{2}$$

$$I_0 = \frac{V_s}{2R}$$

$$2. \quad \underline{I_2 \rightarrow ON}$$

$$\therefore V_0 = -\frac{V_s}{2}, \quad I_0 = -\frac{V_s}{2R}$$

$$\rightarrow V_{or} = \frac{V_s}{2}$$

$\rightarrow$ Forced commutation is required for

Resistive load.

$\rightarrow$ Feed back diodes won't conduct for resistive load.

Harmonic Analysis for o/p v_g waveform

(2)

Finding the Fourier series for the o/p waveform, we get,

$$V_0 = \sum_{n=1,3,5,\dots}^{\infty} \frac{4\left(\frac{V_s}{2}\right)}{n\pi} \sin n\omega t$$

$$\Rightarrow V_0 = \sum_{n=1,3,5}^{\infty} \frac{2V_s}{n\pi} \sin n\omega t$$

$$= \frac{2V_s}{\pi} \left[\sin \omega t + \frac{\sin 3\omega t}{3} + \frac{\sin 5\omega t}{5} + \dots \right]$$

$$V_{on} = \frac{2V_s}{n\pi} \sin n\omega t$$

$$(V_{on})_{rms} = \frac{\frac{2V_s}{n\pi}}{\sqrt{2}} = \frac{\sqrt{2}V_s}{n\pi}$$

$$(V_{o1})_{rms} = \frac{\sqrt{2}V_s}{\pi}$$

$$\text{Distortion factor } f = \frac{(V_{o1})_{rms}}{V_{or}} = \frac{\frac{\sqrt{2}V_s}{\pi}}{\frac{2V_s}{\pi}} = \frac{\sqrt{2}}{2}$$

④
→ g value depends only on the shape of the waveform.

$$\rightarrow \text{THD} = \left(\frac{1}{g^2} - 1 \right)^{1/2}$$

$$= \sqrt{\frac{\pi^2}{8} - 1}$$

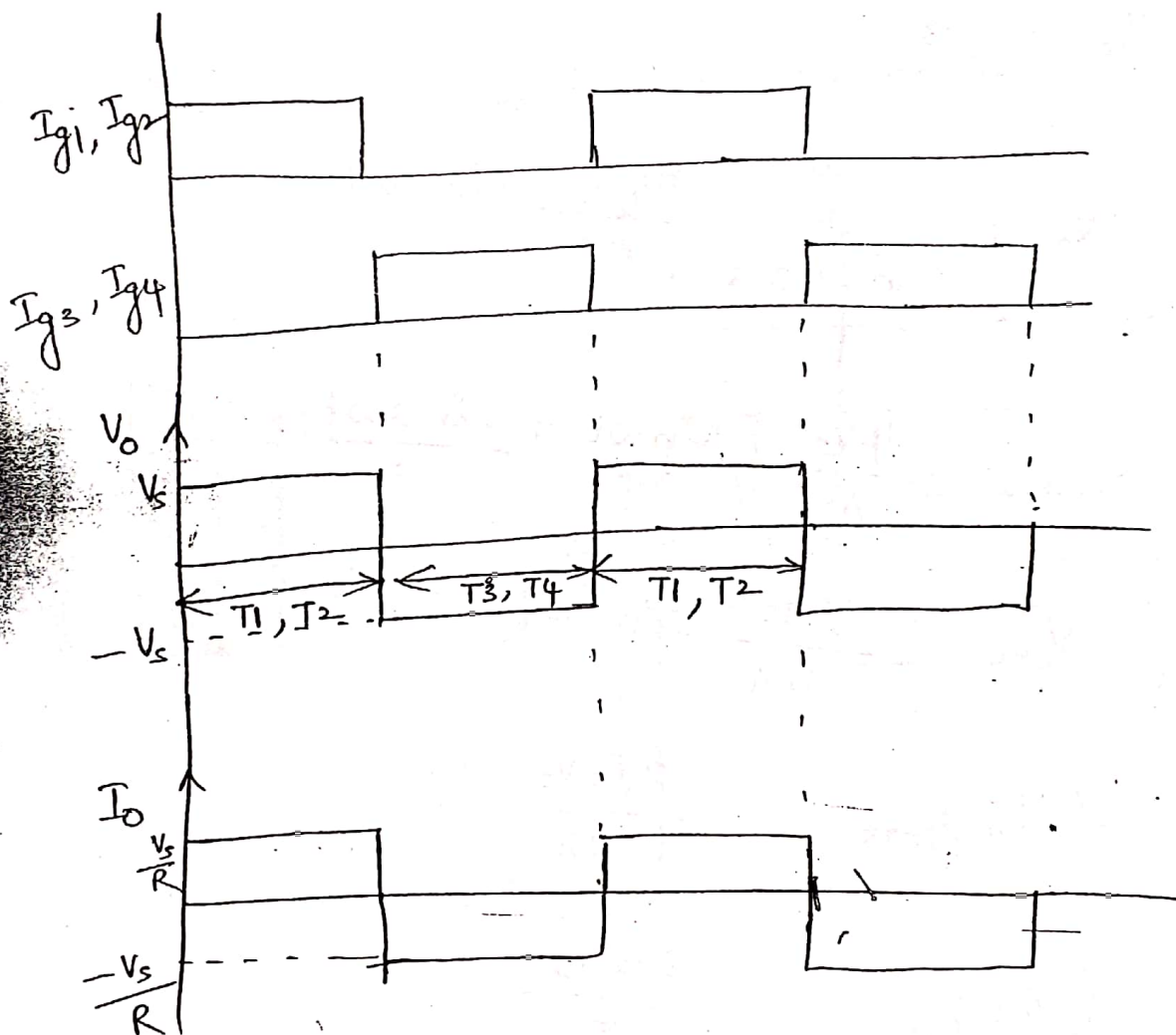
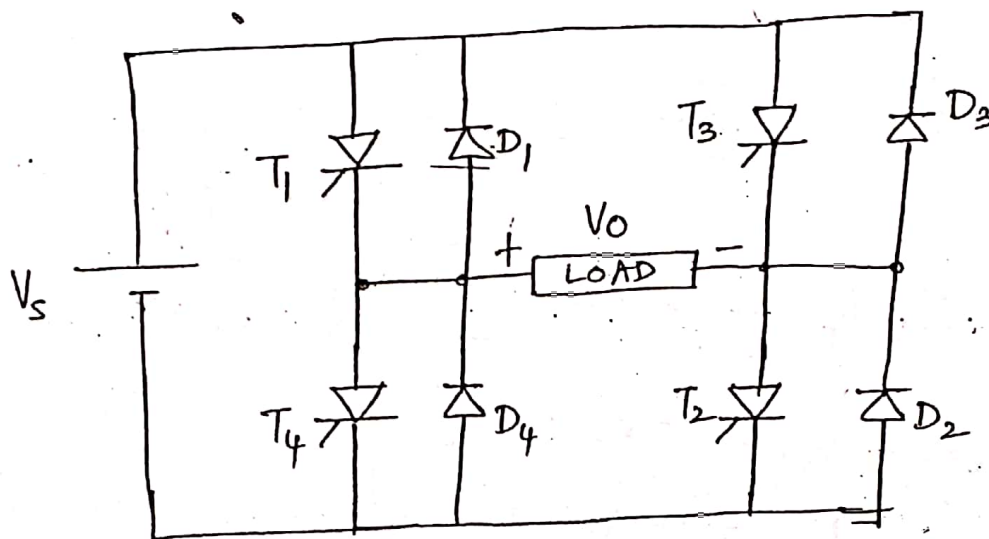
$$= 0.4834$$

$$\boxed{\text{T.H.D.} = 48.34 \%}$$

→ T.H.D. is a measure of harmonics in AC waveform.

* → In a $V \propto I$, of v_g waveform remains same irrespective of nature of the load. The load current magnitude & load waveform changes as the load nature of the load changes.

1- ϕ Full bridge Inverter



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$$1) T_1, T_2 \rightarrow ON$$

$$V_o = V_s$$

$$I_o = \frac{V_s}{R}$$

$$2) T_3, T_4 \rightarrow ON$$

$$V_o = -V_s$$

$$I_o = \frac{-V_s}{R}$$

$$\rightarrow V_{or} = \frac{V_s}{8}$$

$$V_o = \sum_{n=1,3,5}^{\infty} \frac{4V_s}{n\pi} \sin n\omega t$$

$$= \frac{4V_s}{\pi} \left[\sin \omega t + \frac{\sin 3\omega t}{3} + \dots \right]$$

$$V_{on} = \frac{4V_s}{n\pi} \sin n\omega t$$

$$(V_{on})_{rms} = \frac{2\sqrt{2}V_s}{n\pi}$$

$$(V_{o1})_{rms} = \frac{2\sqrt{2}V_s}{\pi}$$

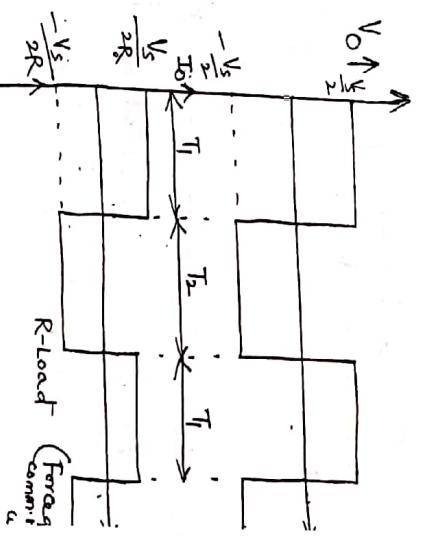
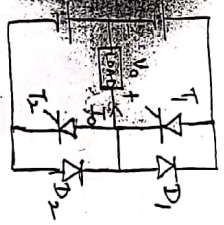
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$$g = \frac{(V_{o1})_{rms}}{V_{or}}$$

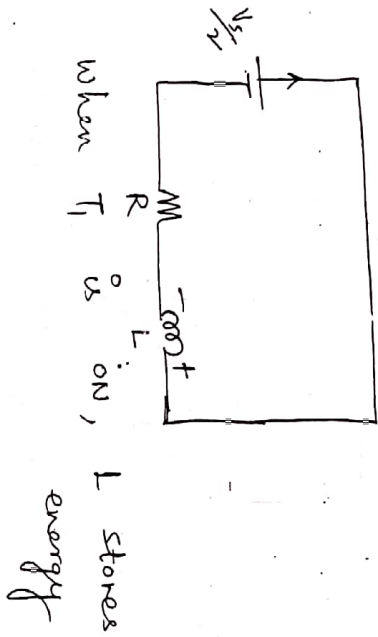
$$= \left[\frac{2\sqrt{2}V_s}{\pi} \right] \frac{1}{V_s} = \frac{2\sqrt{2}}{\pi}$$

$$T.H.D = \sqrt{\frac{1}{g^2} - 1} = 48.34\%$$

1 @ 1- ϕ half bridge Inverter

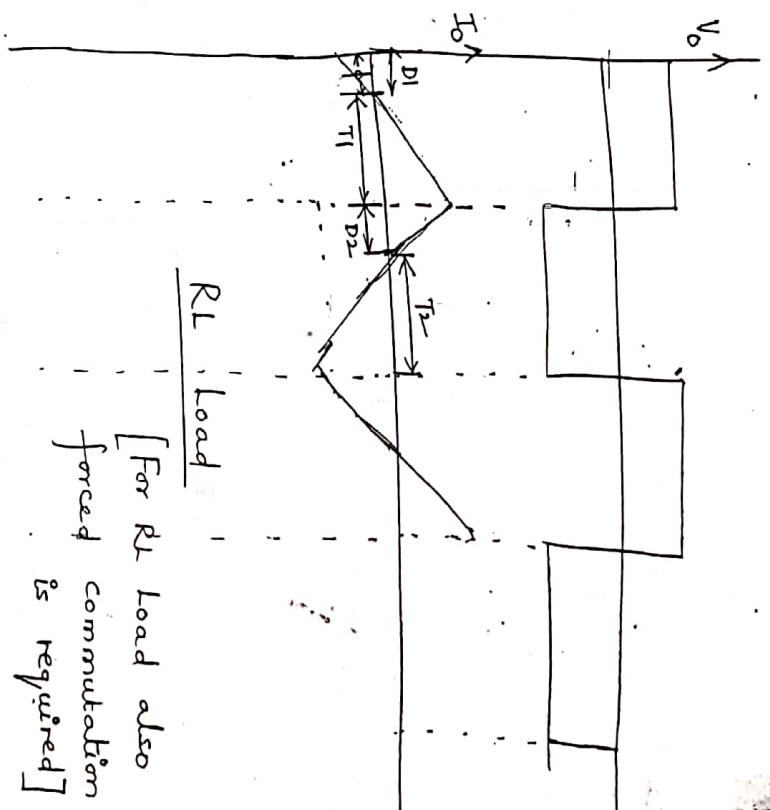


→ For RL Load I_o lags V_o by $\phi = \tan^{-1}\left(\frac{\omega L}{R}\right)$



⇒ Power flows from source to load.
 $\left(\frac{1}{2} Li^2 + i^2 R\right)$

- (i) when thyristor is ON, Power is stored $\therefore L$ stores energy
- (ii) The moment thyristor is forced commutated, inductor starts releasing its energy to source through Diode.

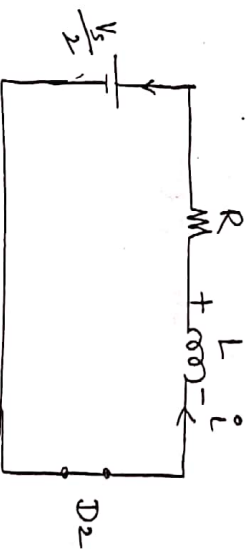


[For RL load also forced commutation is required]

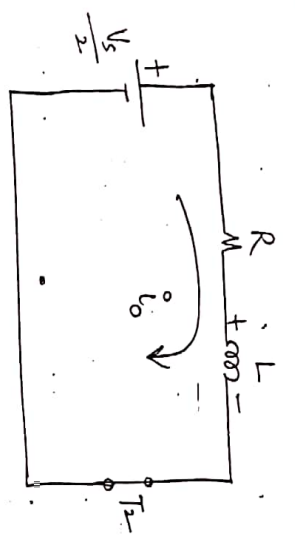
Mode 2

D_2 is ON

$$\frac{1}{2} Li_o^2 \rightarrow P_{source} + i^2 R$$

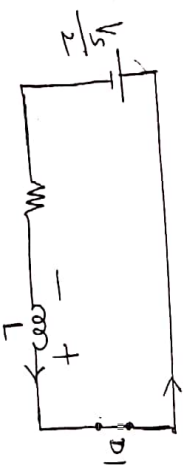


Mode 3: T_2 is ON



$P_{source} \rightarrow -\frac{1}{2} Li^2 + i^2 R$

Mode 4: $D1$ is ON



$\frac{1}{2} Li^2 \rightarrow P_{source} + i^2 R$

(10)

$T_1 \rightarrow ON$
 or $D_1 \rightarrow ON$ } $V_o = \frac{V_s}{2}$ i.e., V_o is +ve

$T_2 \rightarrow ON$
 or $D_2 \rightarrow ON$ } $V_o = -\frac{V_s}{2}$ i.e., V_o is -ve

(11)

Logic Table

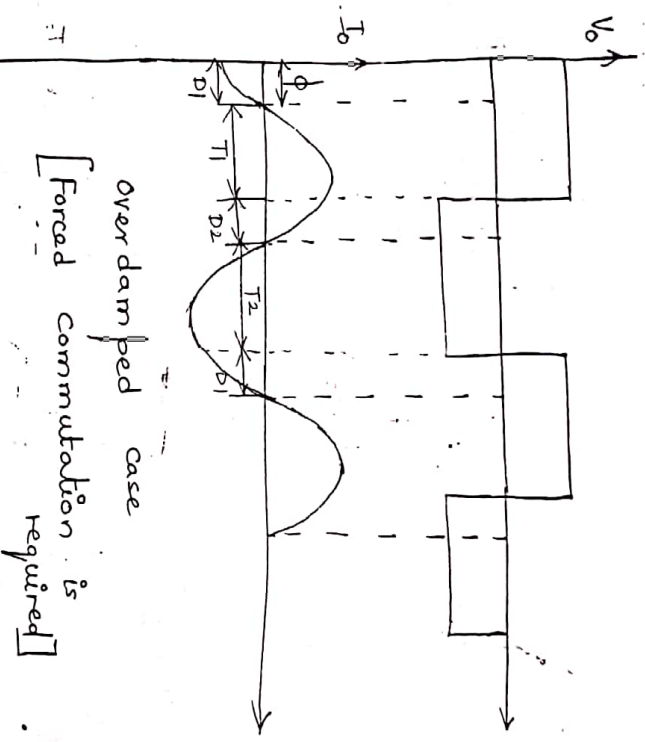
V_o	I_o	P	$\frac{1}{2}$ Bridge	Device (ON)
+	+	+	(T_1, D_1)	T_1
+	-	-	(T_1, D_2)	T_1, T_2
-	-	+	(D_1, D_2)	D_1, D_2
-	+	-	(T_2, D_2)	T_2
-	+	-	(D_1, D_2)	T_3, T_4
-	+	-	(T_2, D_2)	D_3, D_4

(12)

(iii) RLC (overdamped case) -

i.e., $(X_L > X_C)$

I_o lags V_o by $\phi = \tan^{-1}\left(\frac{X_L - X_C}{R}\right)$



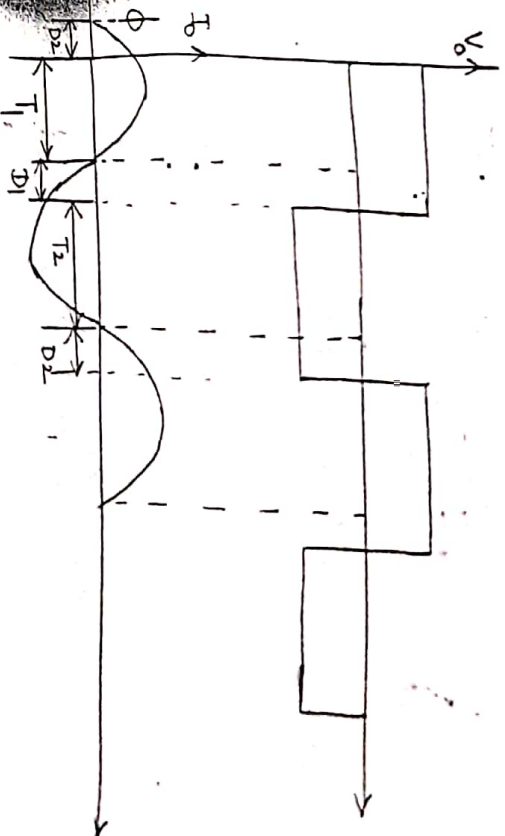
(13)

RLC (Underdamped case)

i.e., $X_C > X_L$ (a $R^2 < \frac{4L}{C}$)

I_o leads V_o by $\tan^{-1}\left(\frac{X_C - X_L}{R}\right)$

(or I_o lags V_o by $\tan^{-1}\left(\frac{X_L - X_C}{R}\right)$)



Underdamped Case

[Load commutation takes place]

→ In this case t_c of π is same as conduction time of $D1$ i.e., $\frac{\phi}{\omega}$

i.e., $t_c = \tan^{-1}\left(\frac{X_C - X_L}{R}\right) \frac{1}{\omega}$

(14)

→ If $t_c > t_q$, forced commutation is not required because load commutation takes place.

→ If $t_c < t_q$, forced commutation is required.

Harmonic analysis for of current for a generalised (half-bridge) load:

→ Let RIC be the generalised load.

$$V_{on} = \frac{2V_s}{n\pi} \sin n\omega t$$

$$Z_n = R + j(X_{Ln} - X_{Cn})$$

$$X_{Ln} = n\omega L$$

$$X_{Cn} = \frac{1}{n\omega C}$$

$$|Z_n| = \sqrt{R^2 + (X_{Ln} - X_{Cn})^2}$$

(15)

$$I_{on} = \frac{V_{on}}{|Z_n|} \angle -\phi_n$$

$$\phi_n = \tan^{-1} \left(\frac{X_{Ln} - X_{Cn}}{R} \right)$$

Here ϕ_n is n^{th} harmonic displacement angle.

$$I_{on} = \frac{2V_s}{n\pi |Z_n|} \sin(n\omega t - \phi_n)$$

⇒ For pure inductive load,

$$|Z_n| = n\omega L$$

$$\phi_n = 90^\circ$$

$$I_{on} = \frac{V_{on}}{n\omega L} \angle -90^\circ$$

$$= \frac{2V_s}{n\pi(n\omega L)} \sin(n\omega t - 90^\circ)$$

$$= \frac{2V_s}{n^2(\pi\omega L)} \sin(n\omega t - 90^\circ)$$

[For square waveform, n^{th} harmonic $\propto \frac{1}{n}$
Triangular " , n^{th} harmonic $\propto \frac{1}{n^2}$]