

2010

AUGUST

SUB:- Digital

Electronics

Branch:- EE (4th sem)

TUESDAY

WEEK 36 • 243-122

Assignment NO:- 4

Date :- 22/04/2020

- Q1 What do you mean by Race-around Condition? Discuss mechanism by which it can be avoided.
- Q2 Design and explain the process of designing MOD-10 ripple counter.
- Q3 Design and explain the process of designing MOD-10 Synchronous counter.
- Q4 Design 6-bit Sequence Generator for sequence 101011
- Q5 Explain the working of parallel to serial converter.